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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/765,910
Filing Date	1/29/2004
First Named Inventor	Manger
Art Unit	2812
Examiner Name	TBD
Attorney Docket Number	INF-131

Sheet 1 of 1

U.S. PATENT DOCUMENTS


Examiner Initials*	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
CM	1	US-6,413,802 B1	07-02-2002	Hu, et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	†
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	†
CM	2	YU, B., et al., "FinFET Scaling to 10nm Gate Length," IEDM, 2002, pp. 251-254.	

Examiner Signature		Date Considered	9/23/05
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